





### **Surface Mount TVS Diodes**

# **Ultra Low Capacitance ESD Protection Diode, TS04021D05V0R3**



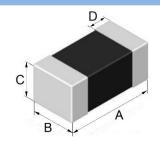
# **Applications:**

- USB2.0 and USB3.0
- HDMI1.3 and HDMI1.4
- Cellular phones
- DVI
- Notebook
- PCI express

### **Features:**

- Transient protection for high-speed data lines.
- IEC61000-4-2 (ESD) ±20KV (air)
- IEC61000-4-2 (ESD)±20KV (contact)
- IEC61000-4-4 (EFT) 20A (5/50ns)
- Cable discharge event (CDE).
- Package optimized for high-speed lines.
- Ultra-small package (1.0 x 0.6 x0.5) (mm).
- Protects one data, controller or power line.
- Ultra-low capacitance: 0.3pF (typical)
- Low leakage current: 1nA @ V<sub>RWM</sub> (typical)
- Low clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for ±8KV contact discharge.

### **Recommended Foot Print Dimensions:**



Unit	mm	
А	1.00±0.05	
В	0.60±0.05	
С	0.50±0.05	
D	0.20±0.05	

# **Part Numbering:**

TS - 0402 - 1D - 05V - 0R3 A B C D E

A: Series

B: Dimension A x B (Inch)

C: Semiconductor TVS Device

D: Reverse Working Voltage 05V=5V E: Parasitic Capacitance 0R3=0.3pF





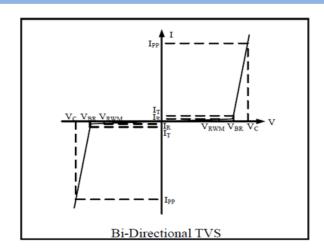


# Electrical Characteristics(@25 $^{\circ}$ C):

Symbol	Parameter	
$V_{RWM}$	Nominal Reverse Working Voltage	
$I_R$	Reverse Leakage Current @ V <sub>RWM</sub>	
V <sub>BR</sub>	Reverse Breakdown Voltage @ I⊤	
I <sub>T</sub>	Test Current for Reverse Breakdown	
Vc	Clamping Voltage @ IPP	
IPP	Maximum Peak Pulse Current	
CESD	Parasitic Capacitance	
VR	Reverse Voltage	
f	Small Signal Frequency	



- 1. Operating temperature range: -55~125 $^{\circ}\!\mathbb{C}$
- 2. Storage temperature range: -55~150 $^{\circ}$ C



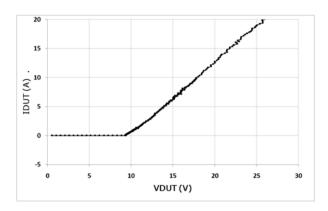
Characteristic	Condition	Value			
Characteristic	Condition	Min.	Typical	Max.	
ESD per IEC61000-4-2 Contact Discharge			±20kV		
ESD per IEC61000-4-2 Air Discharge			±20kV		
Peak Pulse Power $(P_{pk})$	t <sub>p</sub> =8/20μs		54W		
Reverse Stand-Off Voltage $(V_{RWM})$				5V	
Reverse Breakdown Voltage $(V_{BV})$	$I_{BV}$ =1mA	6V	8V		
Clarating Valtage (II.)	$I_{pp}$ =1A, $t_p$ =8/20µs	12V			
Clamping Voltage ( $V_{CL}$ )	$I_{pp}$ =3A, $t_p$ =8/20µs			18V	
Junction Capacitance $(C_J)$	V <sub>RWM</sub> =0V, f=1MHz		0.3pF		
Reverse Leakage Current (I <sub>RM</sub> )	V <sub>RWM</sub> =5V			0.1µA	
Olamain a Vallana (V.)	$I_{pp}$ =16A, $t_p$ =100ns, TLP			25V	
Clamping Voltage $(V_{CL})$	V <sub>ESD</sub> =8KV			25V	



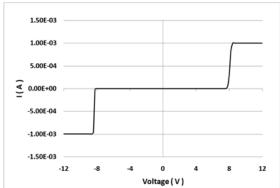




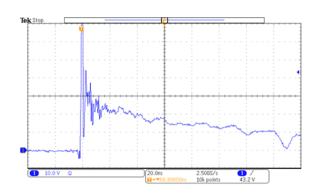
### **TLP Measurement**



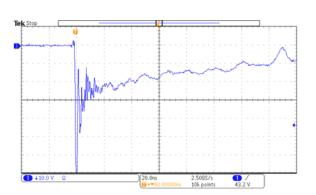
# Voltage Sweeping of I/O\_1 to I/O\_2



ESD Clamping of I/O\_1 to I/O\_2 (+8kV Contact per IEC 61000-4-2)

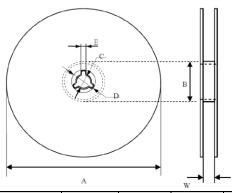


ESD Clamping of I/O\_1 to I/O\_2 (-8kV Contact per IEC 61000-4-2)



# **Packaging Information:**

### **Reel Dimension:**



Type	Α	В	С	D	E	W
7"x8mm	178.0±1.0	60.0±0.5	13.0±0.2	21.0±0.2	2.0±0.5	9.0±0.5

Units: mm

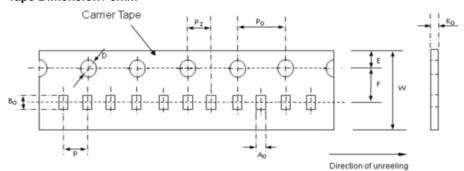
### **Packaging Quantity:**

Size	1 REEL
Size	Quantity
0402	10000









Series	A0	В0	K0	D	Р	P2	P0	W	E	F
0402	0.70±0.05	1.12±0.05	0.60±0.05	1.50+0.10/-0.05	2.00±0.10	2.00±0.10	4.00±0.10	8.00±0.10	1.75±0.10	3.50±0.05

Units: mm

# **Mechanical Characteristics:**

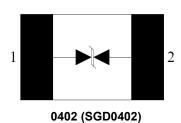
Case: 0402

Flammability rating: UL 94V-0
 Packaging: tape and reel
 Polarity: bidirectional

### **Circuit Diagram**



### **Pin Configuration**



(TOP View)

# **Reliability and Test Condition:**

Item	Performance Test Condition
High Temperature Reverse Bias (HTRB)	1000 hours at the maximum DC Reverse Voltage rated junction temperature specified in the user/supplier specification. The ambient temperature TA is to be adjusted to compensate for current leakage. TEST before and after HTRB as a minimum. (See note X HTRB.) Test condition: 125±3 °C; Bias at 80% VBR; 1000 hours
Temperature Cycling (TC)	1000 cycles (TA = minimum range of -55°C to maximum rated junction temperature, not to exceed 150°C). Can reduce duration to 400 cycles using TA (max) = 25°C-over part maximum rated junction temperature or using TA (max) = 175°C if the maximum rated junction temperature is above 150°C. TEST before and after TC. Test condition: $-55^{\circ}$ C / +150°C / 15 minutes; 1000 cycle
High Humidity High Temp. Reverse Bias (H3TRB)	1000 hours at TA = $85^{\circ}$ C/ $85\%$ RH with part reverse biased at $80\%$ of rated breakdown voltage up to a maximum of 100V or limit of chamber. TEST before and after H3TRB as a minimum. Test condition: $85^{\circ}$ C / $85\%$ RH, Bias at VRMW , 1000 hours
Intermittent Operational Life (IOL)	Tested per duration indicated in Table 2A. TA=25°C. Parts powered to insure Δ TJ ≥ 100°C (not to exceed absolute maximum ratings). TEST before and after IOL as a minimum.  Test condition: On=2 minutes Bias at VRWM, Off=2 minutes  15000 Cycle
Autoclave (AC)	96 hours, TA = 121°C, RH = 100%, 15psig. TEST before and after AC. Test condition: 96 hours, TA = 121°C, RH = 100%, 15psig.

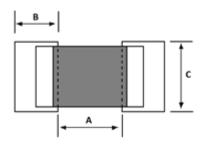






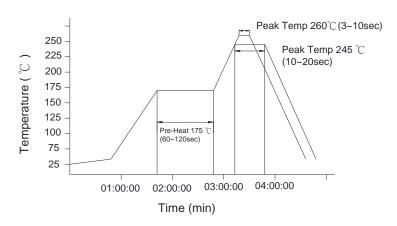
## **Soldering and Mounting:**

#### Recommended PC Board Pattern



Series	A(mm)	B(mm)	C(mm)
0402	0.3~0.5	0.5~0.7	0.6~0.7

### The IR reflow and temperature of soldering for Pb free process



### IR reflow Pb free process suggestion profile

- The solder recommend is Sn96.5/Ag3.5 of 80 to 130μm
- Ramp-up rate (217°C to peak) +3°C/second max.
- Temp. maintain at 175±25℃ 180 seconds max.
- Temp. maintain above 217 ℃ 60~150 seconds
- Peak temperature range 245 +20/-10<sup>∞</sup> within 5<sup>∞</sup> of actually peak temperature (t<sub>p</sub>) 10~20 seconds
- Ramp down rate -6 ℃/second max.
- Steel plate thickness 0.08~0.10mm

#### Hand soldering

In hand soldering of the devices, large temperature gradient between preheated the devices and the tip of soldering iron may cause electrical failures and mechanical damages such as cracking or breakings of the devices. The soldering shall be carefully controlled and carried out, so that the temperature gradient is kept minimum with following recommended conditions for hand soldering.

#### Recommended soldering condition 1 (with preheating)

#### Solder

0.12~0.18mm thread solder (Sn96.5:Ag3.5) with soldering flux in the core rosin-based and non-activated flux is recommended.

#### Preheating

The devices shall be preheated so that temperature gradient between the devices and the tip of soldering iron is  $150\,^{\circ}$ C or below.

#### Soldering iron

Rated power of 20W max. with 3mm soldering tip in diameter

Temperature of soldering iron tip  $300^{\circ}$ C max. 3-5sec (The required amount of solder shall be melted in advance on the soldering tip.)

### Cooling

After soldering, the devices shall be cooled gradually at room ambient temperature.



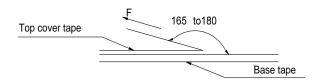




### Recommended soldering condition 2 (without preheating)

- Temperature of soldering iron tip 300 ℃ max. 3-5sec
- Solder iron tip shall not directly touch to the devices.
- Solder iron tip shall be fully preheated before soldering while soldering iron tip to the external electrode of the devices.

# **Tearing Off Force:**



The force for tearing off cover tape is 10 to 100 grams in the arrow direction under the following conditions.

Room	Room	Room	Tearing
Temp.	Humidity	atm	Speed
(℃)	(%)	(hPa)	mm
5~35	45~85	860~1060	300±10

## **Storage Conditions (Component level):**

- Temperature and humidity conditions: Less than 40 ℃ and 60% RH.
- Recommended products should be used within 12 months from the time of delivery.
- The packaging material should be kept where no chlorine or sulfur exists in the air.

### **Transportation:**

- Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- The use of tweezers or vacuum pick up is strongly recommended for individual components.
- Bulk handling should ensure that abrasion and mechanical shock are minimized.







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